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FOREIGN PATENT DOCUMENTS							T ⁶
Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office ³	Number ⁴	Kind Code ⁵ (If known)			
Jm		JP	0904443		Patent Abstracts of Japan – 1 page; Japanese Patent – 13 pages, not translated; Applicant: NEC ENG. LTD.; Inventor: Yoshida Hirokazu	02-14-1997	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390P11617	Application Number: 09/992,772
Page 1 of 1		First Named Inventor: John I. Garney et al.	
		Filing Date: November 5, 2001	
OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
JMP		U.S. Serial No. 09/361,677 by John I. Garney et al., filed July 27, 1999. <i>noted</i>	
JMP		U.S. Serial No. 09/362,991 by John I. Garney et al., filed July 27, 1999. <i>noted</i>	
JMP		U.S. Serial No. 09/461,625 by John I. Garney, et al., filed December 14, 1999. <i>noted</i>	
JMP		U.S. Serial No. 09/473,914 by John I. Garney, et al., filed December 28, 1999. <i>noted</i>	
JMP		Enhanced Host Controller Interface Specification for Universal Serial Bus dated November 10, 2000, Revision: 0.95; cover pages - iv and pages 1-130.	
JMP		Enhanced Host Controller Interface Specification for Universal Serial Bus dated May 30, 2001, Revision 0.96 rc2, cover pages - iv and pages 1-142.	

Examiner Signature	<i>Sam Ray</i>	Date Considered	10/30/03
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